

### REMARKS

Claims 30-76 are pending in this application, with claims 30, 35, 40, 45, 49, 53, 58 and 64 being independent. Claims 32-39, 42-44, 47, 48, 51, 52, 55, 56, 63 and 69 have been previously withdrawn. New claims 70-76 have been added. Support for new claims 70-76 may be found in the application at least at page 28 lines 16-19 and lines 23 and 24. Claims 30, 40, 45, 49, 53, 58 and 64 have been amended. No new matter has been introduced.

Specifically, claim 30 has been amended to recite "an integrated circuit including a *microprocessor*" (emphasis added), "wherein the sensor is connected to the portable apparatus" and "wherein the integrated circuit is *entirely* incorporated into the sealing material" (emphasis added). Support for the amendment may be found in the application at least at page 8, lines 27-29.

Claim 40 has been amended to recite "a controller including a *CPU*" (emphasis added), "wherein the sensor is connected to the portable apparatus" and "wherein the controller is *entirely* sealed by the liquid crystal material" (emphasis added). Support for the amendment may be found in the application at least at page 22, lines 20 and 21; page 31, lines 9-16; and Fig. 18.

Claim 45 has been amended to recite "a controller including a *CPU*" (emphasis added), "wherein the sensor is connected to the portable apparatus" and "wherein the controller is *entirely* incorporated into the sealing material" (emphasis added). Support for the amendment may be found in the application at least at page 22, lines 20 and 21; and page 8, lines 27-29.

Claim 49 has been amended to recite "a *CPU* comprising a plurality of TFTs" (emphasis added) and "wherein the CPU is *entirely* sealed by the liquid crystal material" (emphasis added). Support for the amendment may be found in the application at least at page 20, lines 14-19; page 31, lines 9-16; and Fig. 18.

Claim 53 has been amended to recite "a controller including a *CPU*" (emphasis added), "wherein the sensor is connected to the portable apparatus," and "wherein the controller is *entirely* incorporated into the sealing material" (emphasis added). Support for the amendment may be found in the application at least at page 22, lines 20 and 21; and page 8, lines 27-29.

Claim 58 has been amended to recite "a controller including a *CPU*" (emphasis added), "a sensor connected to the information system," and "wherein the controller is *entirely*

incorporated into the sealing material" (emphasis added). Support for the amendment may be found in the application at least at page 22, lines 20 and 21; and page 8, lines 27-29.

Claim 64 has been amended to recite "a controller including a *CPU* for controlling the driver circuit, the controller provided over the substrate" (emphasis added), "a sensor section connected to the information system," and "wherein the controller is *entirely* sealed by the liquid crystal material" (emphasis added). Support for the amendment may be found in the application at least at page 22, lines 20 and 21; page 31, lines 9-16; and Fig. 18.

Independent claims 30, 40, 45, 49, 53, 58 and 64, and their dependent claims 31, 41, 46, 50, 54, 57, 59-62 and 65-68, have been rejected as being unpatentable over Yamano (US 4,743,122) in view of Sawatsubashi (US 5,148,301). Independent claim 30, as amended, recites an integrated circuit that is entirely incorporated into the sealing material and that includes a microprocessor. Independent claim 40 recites a controller that is entirely sealed by the liquid crystal material and that includes a CPU. Applicants request reconsideration and withdrawal of the rejections of claims 30 and 40, and their dependent claims, because neither Yamano, Sawatsubashi, nor any proper combination of the two describes or suggests the integrated circuit recited in claim 30 and the controller recited in claim 40.

Yamano describes an infrared temperature measuring apparatus having a liquid crystal displaying apparatus 105. As acknowledged by the Examiner on page 2 of the Final Office Action, Yamano does not describe the details of the liquid crystal displaying apparatus 105 and, therefore, necessarily fails to describe or suggest an integrated circuit having a microprocessor that is entirely incorporated into a sealing material and a controller having a CPU that is entirely sealed by a liquid crystal material, as claimed. The Examiner turns to Sawatsubashi to remedy this deficiency.

Sawatsubashi describes a liquid crystal display device that includes gate line driving circuits 113, which the Examiner equates to each of the recited controller and the recited integrated circuit. Notably, the gate line driving circuits 113 do not include a microprocessor or a CPU. Rather, gate line driving circuits 113 include an integrated circuit having a plurality of thin film transistors for controlling TFTs 104 of pixel electrodes 103 to thereby enable drain line driving circuits 112 to supply data signals to the pixel electrodes 103. See col. 4, lines 50-62.

Moreover, gate line driving circuits 113 are not entirely incorporated into seal member 108, which the Examiner equates to the recited sealing material and are not entirely sealed by liquid crystal 109. Rather, as shown in Figs. 3 and 4 and described in col. 4, lines 62-66 of Sawatsubashi, gate line driving circuits 113 have an outer edge that is overlapped with the seal member 108 and an inner edge that extends into the region in which the liquid crystal 109 is sealed.

For at least these reasons, applicants request reconsideration and withdrawal of the rejections of claims 30 and 40, and their dependent claims.

As stated above, claims 45, 53 and 58 have been amended to recite a controller including a CPU that is entirely incorporated into a sealing material; claim 49 has been amended to recite a CPU that is entirely sealed by a liquid crystal material; and claim 64 has been amended to recite a controller including a CPU that is entirely sealed by the liquid crystal material. For at least the reasons described above, applicants request reconsideration and withdrawal of the rejection of claims 45, 49, 53, 58 and 64, and their dependent claims, because neither Yamano, Sawatsubashi, nor any proper combination of the two describes or suggests these features.

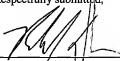
Applicants submit that all claims are in condition for allowance.

The fees in the amount of \$470 for the one-month extension of time fee (\$120) and the extra dependent claims fee (\$350) are being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

3/21/08

  
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